As a below named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original first and joint inventor (if more than one individual inventor is listed below) of the invention in

SEMICONDUCTOR DEVICE HAVING A WIRING LAYER OF DAMASCENE STRUCTURE AND METHOD FOR MANUFACTURING THE SAME

the	specification ked.	o f	which	is	attached	hereto	unless	the	following	$b \circ \mathbf{x}$	is
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□ was filed on as United States Application or PCT International Application No. (if applicable). was amended on

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56. I hereby claim foreign priority benefits under 35 U.S.C. I hereby claim foreign priority benefits under 35 U.S.C. 119(a)—(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at

least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

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113 Priority Country Category Application No. Filing Date Claim L Japan Patent 2000-185152 June 20, 2000 Yes 123

And I hereby appoint Donald W. Banner (Reg. No. 17, 037), Harold J. Birch (Reg. No. 16, 527), Edward F. McKie, Jr. (Reg. No. 17, 335), William W. Beckett (Reg. No. 18, 262), Dale H. Hoscheit (Reg. No. 19, 090), Joseph M. Potenza (Reg. No. 28, 175), Alan I. Cantor (Reg. No. 28, 163), James A. Niesowski (Reg. No. 28, 331), Barry L. Grossman (Reg. No. 30, 844), Joseph M. Skerbon (Reg. No. 29, 864), Thomas L. Peterson (Reg. No. 30, 969), Nina L. Skerbon (Reg. No. 29, 864), Thomas L. Peterson (Reg. No. 30, 193) and Thomas H. Jackson (Reg. No. 29, 864), Thomas L. Peterson (Reg. No. 32, 193) and Thomas Street, N. W. Washinston, D. C. 20001-4597, address is 11th Floor 10010 Street, N. W. Washinston, D. C. 20001-4597, and revocation to prosecute this. neys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent & Trademark Office connected therewith, and request that correspondence be directed to Banner & Witcoff, Ltd., 11th Floor, 1001 G Street, N.W., Washington, D.C. 2001-4597.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States tode and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION FOR PATENT APPLICATION

I declare further that my mailing address is at c/o Intellectual Property Division. KABUSHIKI KAISHA TOSHIBA, 1-1 Shibaura 1-chome. Minato-ku. Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

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